Application Data Sheet

Application Information

Application Type:: Regular

Subject Matter:: Utility

Suggested Group Art Unit:: Not Yet Assigned

CD-ROM or CD-R?:: None

Sequence submission?:: None

Computer Readable Form (CRF)?:: No

Title:: MODEL STAMPING MATRIX CHECK

TECHNIQUE IN CIRCUIT SIMULATOR

Attorney Docket Number:: 188122000700

Request for Early Publication?:: No

Request for Non-Publication?:: No

Total Drawing Sheets:: 5

Small Entity?:: No

Petition included?:: No

Secrecy Order in Parent Appl.?:: No

Applicant Information

Applicant Authority Type:: Inventor

Primary Citizenship Country:: China

Status:: Full Capacity

Given Name:: Yutao

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State or Province of Residence:: CA

Country of Residence:: US

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City of mailing address:: San Jose

State or Province of mailing address:: CA

Postal or Zip Code of mailing address:: 95126

Applicant Authority Type::

Primary Citizenship Country:: US

Status:: Full Capacity

Given Name:: Bruce

Family Name:: MCGAUGHY

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State or Province of Residence:: CA

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Inventor

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Applicant Authority Type:: Inventor

Primary Citizenship Country:: US

Status:: Full Capacity

Given Name:: Zhihong

Family Name:: LIU

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State or Province of mailing address:: CA

Postal or Zip Code of mailing address:: 95014

Correspondence Information

Correspondence Customer Number:: 25226

Representative Information

Representative Customer Number:: 25226

Initial: 2/6/04

Assignee Information

Assignee name::

Cadence Design Systems, Inc.